

SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

(Sponsored by Sri Vasavi Educational Society) (Approved by AICTE, New Delhi & Permanently affiliated to JNTUK, Kakinada) (Accredited by NAAC with 'A' Grade, Recognized by UGC under section 2(f) & 2(B)) (NBA Accreditation to B.Tech. EEE, CSE, ME and ECE Branches for 3 Years) Pedatadepalli, TADEPALLIGUDEM – 534 101. W.G.Dist. (A.P)

3.1.1. Research equipment / Software details

S.No.	Department	Page No.
1.	Department of Electronics and Communication	1
	Engineering	
2.	Department of Electrical and Electronics	11
	Engineering	
3.	Department of Computer Science and	14
	Engineering	
4.	Department of Mechanical Engineering	16

1. Department of Electronics and Communication Engineering Research Equipments and Software's

ACADEMIC YEAR 2022-2023

A.Advanced VLSI Design Laboratory with Cadence software

The Advanced VLSI Design Laboratory is established with the objective of providing the undergraduates, postgraduates and PhD students with an in-depth knowledge of Full-custom and Semi-custom IC Design. It offers basic and advanced facilities in the area of VLSI device modeling and simulation using industry-standard Cadence® EDA design tool (10 full suite licenses available) supported by high performance hardware tools.

Analog Integrated Circuit Design Tools & Digital Integrated Circuit Design Tools: ASSURA41, CONFRML211, GENUS 211, IC618, INCISIVE152, INNOVUS211, LMGR, PEGASUSDFM221, PVS222, QUANTUS212, SPECTRE211, XCELIUM2209.





Faculty member engaged in research and development activities within the R&D laboratory and utilizing Cadence software.

B.ECAD LAB with Vivado Software

ECAD LAB is equipped with Vivado software and latest FPGA Boards.

Vivado is a comprehensive software suite developed by Xilinx, designed primarily for the development, synthesis, and implementation of digital designs targeting Xilinx Field Programmable Gate Arrays (FPGAs) and Complex Programmable Logic Devices (CPLDs). It provides a range of tools and features for designing, optimizing, and analyzing digital circuits and systems. Vivado software finds diverse applications in research across various domains, including:

Digital System Design: Researchers can use Vivado to design and implement complex digital systems, exploring novel architectures and optimizing for specific metrics like speed or energy efficiency.

Signal Processing: Vivado can be employed for accelerating signal processing algorithms by mapping them onto FPGAs, offering potential speedup over traditional software implementations.

Embedded Systems: Researchers can design custom hardware accelerators for embedded systems, enhancing their performance and efficiency.

Communication Systems: Vivado can be used to implement digital communication systems, enabling the exploration of advanced modulation/demodulation techniques and channel coding.

Image and Video Processing: Vivado's capabilities can be leveraged to accelerate image and video processing tasks, such as image filtering, compression, and feature extraction.

Machine Learning: Researchers can explore hardware acceleration of machine learning algorithms, taking advantage of FPGAs' parallel processing capabilities.

In essence, Vivado software serves as a versatile tool for researchers to experiment with, prototype, and optimize digital designs across a wide spectrum of research areas. Its comprehensive features and FPGA-specific capabilities make it a valuable asset in advancing various fields of study.

FPGA Development Boards: PYNQ and ZedBoard

PYNQ (Python Productivity for Zynq), created by Xilinx, enables easy programming of Zynq SoCs using Python. PYNQ offers the Pynq-Z1 boards, featuring Xilinx Zynq-7000 APSoC. Developers can leverage Python libraries to accelerate hardware design, create custom IPs, and interact with programmable logic, simplifying embedded system development. PYNQ boards are ideal for quick prototyping and learning FPGA-based design through a familiar programming language and Jupyter notebooks.



Pynq-Z1 boards

ZedBoard is a development board built around the Xilinx Zynq-7000 SoC. It combines a dualcore ARM Cortex-A9 processor with programmable logic, offering a flexible platform for embedded systems development. The ZedBoard allows developers to create custom accelerators, interface with peripherals, and run operating systems, making it suitable for various applications such as signal processing, motor control, and real-time systems prototyping. Its FPGA fabric can be programmed using Xilinx Vivado tools and high-level languages like C/C++ or MATLAB.





Zynq-7000 boards



Faculty members engaged in research and development activities within the Electronic Computer-Aided Design (ECAD) laboratory, collaborating with and utilizing Field-Programmable Gate Array (FPGA) boards.

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C.Internet of Things Lab

IoT Lab helps with all aspects of our digital transformation, including requirements, strategy, and architecture design, as well as technology selection, deployment, integration, test, and validation. The network of physical objects or "things" that are integrated with sensors, software, and other technologies for the purpose of communicating and sharing data with other devices and systems over the internet is the focus of the Internet of Things (IoT) research lab.

It facilitates improved decision-making, saves time and money, and increases process effectiveness, asset utilization, and productivity. Benefits from real-time analytics and insights for better decision-making. IoT will help provide better control of various aspects with regard to applications like healthcare, agriculture, smart cities, etc.



Faculty members conducting research and development activities in the Internet of Things (IoT) Laboratory

Devices such the Beaglebone Black, Raspberry Pi, TI Lunchbox, Pine64, and Maduino with ARM processors, as well as the ESP32, ESP8266, and Arduino Boards with a variety of sensors and actuators, are available in the IoT Lab, giving users the option to use these components in a wide range of applications.



Academic year 2018-2019

A. Digital Signal Processing Lab with MATLAB Software

MATLAB 2018b with advanced tool boxes are purchased for Research purpose. These are useful for faculty and students doing research in Image and signal processing domain. The tool boxes used for research work are shown below

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			Computer Vision System To	olbox	Version 8.2	(R2018b)	
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			Deep Learning Toolbox		Version 12.0	(R2018b)	
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Deep Learning Toolbox provides simple MATLAB commands for creating and interconnecting the layers of a deep neural network. Examples and pre-trained networks make it easy to use MATLAB for deep learning, even without knowledge of advanced computer vision algorithms or neural networks.



Faculty members engaged in research within the Digital Signal Processing (DSP) Laboratory, employing the Deep Learning Toolbox.

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B.HFSS SOFTWARE

ANSYS HFSS (High-Frequency Structure Simulator) is a widely used electromagnetic simulation software developed by ANSYS, Inc. It is primarily designed for simulating and analyzing electromagnetic fields and effects at high frequencies, including radio frequency (RF), microwave, and millimeter-wave frequencies. HFSS is commonly used in various industries, including telecommunications, aerospace, automotive, electronics, and more.

Key features of ANSYS HFSS include:

Electromagnetic Simulation: HFSS provides a comprehensive set of tools for simulating electromagnetic phenomena, such as electromagnetic wave propagation, radiation patterns, impedance matching, and scattering.

Solver Technology: HFSS employs finite element method (FEM) and finite element-boundary integral (FE-BI) methods to accurately model and solve complex electromagnetic problems. It offers various solvers for different scenarios and accuracy requirements.

Geometry Modeling: Users can create intricate geometric models of structures, components, and devices directly within the HFSS interface. The software supports 2D and 3D modeling.

Parametric Analysis: HFSS allows users to perform parametric studies by varying design parameters and analyzing how changes impact electromagnetic behavior. This is useful for optimization and sensitivity analysis.

Antenna Design: HFSS is commonly used for antenna design, allowing engineers to simulate and optimize antenna performance, radiation patterns, and other characteristics.

Waveguide and Transmission Line Analysis: HFSS can simulate the behavior of waveguides, transmission lines, and other RF components, aiding in the design and analysis of RF circuits.

Material Database: The software includes a comprehensive material database with various dielectric and conductor materials to accurately model real-world materials.

Post-Processing: HFSS provides various post-processing tools to visualize simulation results, including field distribution plots, S-parameters, radiation patterns, and more.



Faculty members conducting research and development in a laboratory utilizing the High-Frequency Structure Simulator (HFSS) software.

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2.DEPARTMENT OF ELECTRICAL & ELCTRONICS ENGINEERING

Academic Year 2022-2023

S.No	Date	EQUIPMENT NAME	QTY
1	19/04/2023	SPARTAN 6 DEVELOPMENT BOARD SP6-LX9-DB	1
2		DESKTOP	2





Academic Year 2019-2020

S.No	Date	EQUIPMENT NAME	QTY
1	11/2/2020	DIGITAL AC CLAMP METER	1
2	12/02/2020	BATTERY CHARGER	1
3	7/9/2019	HP DESKTOPS Page 11 of 20	25







Academic Year 2018-2019

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3.DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Academic Year: 2020-21 R&D Lab

Configuration (Server): Server: DELL R-240 Power Edge 1U Rack ServerProcessor: Intel Xeon E-2124 3.3GHZ Number of servers: 01

RAM: 32 GB UDIMM DDR4 ECC RAM (2*16) 2666MHZ.

HDD: 1*1TB 7.2K RPM 6 GEPS 512N 3.5" cabled Hard Drive.

Desktops: 18

Configuration:

Dell U24115H Ultra 24" LED Monitor

Processor: Intel[®] CoreTM i5-7400 CPU @ 3.00 GHz RAM: 4.00 GB





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Doll USB Keyboard				
Dell USB Optical Mouse				
S 1ag. 8Q5DY23				
2 Dell U24115H Ultra 24" LED Monitor 8528 3 Years Warranty 8 LAGS	18 %	18 nos 16	5,072.03 nos	2,89,296.54
6K69W03, FF59W03, C9V6W03, CQ76W03, 28W003, B06DV03, HNCDW03, D575W06, 8457W03,				
JFCDW03, HQCDW03, BCSTW03, JAKCDW03, CHBDW03, 4LCDW03, apr:ruma, BHCDW03, 2CCDW03,				-
				3,95,228.74
CGST@9%			9 %	35,570.59
SGST@9% ROUNDED OFF			9 %	35,570.59 0.08
Total		19 nos		₹ 4,66,370.00
Amount Chargeable (in words)	-			E. & O.I.
INR Four Lakh Sixty Six Thousand Three Hundred Seventy	e Ce	ntral Tax	State Tax	Total
HSN/SAU Value	Rate	Amount I	Rate Amo	unt Tax Amount 3 90 19 067 80
84715000 1.05.932	20 9% 54 9%	26,036.69	9% 26,03	6.69 52.073.38
8528 Total 3,95,228	.74	35,570.59	35,57	0.59 71,141.18
Ter terret (search) INP Seventy One Thousand One Hundred	Forty O	ne and Eigh	iteen paise	Only
in the second seco				V.
		1	Head of the	Recartineant
		Depi. of Sri Ma	Computer is	nerco & Engineering
		TAL	DEPALLIG	UDEM-534 101
Company	's Bank D	Details		State State State State
We declare that this invoice shows the actual price of the Bank Nar acods described and that all particulars are true and correct. A/c No.	me	Canara Bank 29852610000	Occ A/c 00	0002985
Branch & I	rs Code:	enaratni Naç	In/LAS	RTECHNOLOGIES
Customer's Seal and Signature			WAD	Authorised Signatory
	DICTION		18	C Ze

4.DEPARTMENT OF MECHANICAL ENGINEERING

S. No.	Academic Year	Details of Research equipment / Software	Photo	Name of the Laboratory
1.	2018 – 19	Gas Analyzer		Thermal Engineering
2.	2018 – 19	Diesel Smoke meter	NORE: METER NORE:	Thermal Engineering
3.	2018 – 19	Hydraulic press		Manufacturing Process
4.	2020 - 21	CNC Lathe Machine		CAD / CAM

			No. Line of the Advance Li
5.	2023 – 24	Solid Works	International Control Internateand Conteand Control Internatingeand Control
			<form><form> Image: Imag</form></form>



CNC Lathe Machine



Gas Analyzer

	Bangalore-560072 Fne: Site No.18, Survey No.131, 17th KM, Maga 28536501, 65997342, M:9243065580 e-mall: tech. ed55@yahoo.co.in techedequipme Regd, Office: # 118, Garden Villas, Nagarbhavil VAT TIN No.: 29350484383 GST No.: 29AA	di Rond, nt@gma Road, Ban APR013	Bangalo il.com galore-5 1M1ZP	re-560 (60 072,	991 Ph:080-	
The	Principal, Varavi Engineering College	INVOI	CE NO:	G90	Date: 3	1 JAN 201
Ped	atadepalli, enalligudem-534 101	YOUR	ORDE	R : dt.0	01.12.2017	
We	t Godavari Dist.	DELIV	ERY	: B	y Road Tra	insport
0	CTN: 370000000000000	DOCU	MENTS	THRO	UGH :	Direct
SL. No.	Description	1	HSN Code	Qty	Unit Price	Amount
1. 2. 3. 4. 5.	Macleod Gauge - 30 s Gas Analyzer Diesel Smoke Meter 3 7 E COP of Vapour Compression System Thermal Conductivity Measurement Apparatu Liquids Less discount @ IGST Unloading by the Purchaser	s-, 497 9 17% @18%:- Total:-	90	1No. 1No. 1No. 1No.	34,700 2,31,500 1,62,500 93,000 44,000	$\begin{array}{r} 34,700\\ 2,31,500\\ 1,62,500\\ 93,000\\ \underline{44,000}\\ 5,65,700\\ \underline{-96,169}\\ 4,69,531\\ \underline{-84,515,58}\\ \underline{-5,54,046,58}\end{array}$
E.O. &E	(Rs. Five Lakhs Fifty Four Thousand Forty Si Paise Fifty Eight Only)	x &				
Goods Our ro be cha Certifi Sales 7 goods	uses and cannot be taken back. sponthillity extent (immediately after Goods are delivered to the carriers i rged extra (fabil is not paid within 7 days of presentation. Subject to Beng ed that the Goods on which the CST has been charged are not exampled fast Act or the rules made thereunder & the amount charged on account are not more than what is payable under the provision of relevant act or the rules made therein the state of the second state of the state of the second state are not more than what is payable under the provision of relevant act or C	Interest @ 245 aloru Jurisdiander the GSI of Sales Tax of he rules made	Ve p.a. will ction only FAct or the n these	10*	Authorised	Signatory
therew	Bank Details: Allahabad Bank, Branch: Br A/e no.:20022904842 IFS Code: ALLA021	asavanag 0326	gudi, Ba	ngalor	e-560 004.	

The second second	and.						-					
		VASANTHA HYDRAULIC SYSTEI Plot No -7-63, S.V.C.LE., BALANAGER, HYDERABA						S -500037	r	Original for Duplicate fi Triplicate f	Receipient or Transporter or Supplier:	White Pink Yellow
			-	E MAIL . kris	hashayana (igmai	VOIC	9441165834 TF					
	GSTIN Number: Tax is Payable On Reverse Charge (Yes/No):	36AOTPB0209H1Z6 Charge (Yeu/No): N.A.				voic	Transportation Mode: Veh.No			By road AP29V5284		
	Invoice Serial Number		VHS/	080			Date & Time o	r Supply:		HYDERABAD		
	Invoice Date:	of Rece	eiver (I	illed to)			Details of		s of Co	Consignee (Shipped to)		
	Name: SRI VASAVI ENGINEERING COLLEGE Address: Tadepalligudem-S34101, W.G.Dist State: AP State Code: 37 STIP Number:				Name: SR Address: Ta State: AF State Code: 37 GSTIN Number:		I VASAVI ENGINEERING COLLEGE depalligudem-534101,W.G.Dist					
	S Discription fo Goods	HSN Code	Qty L	OM Rate	Total	Discount	Taxable Value		:GST	S Rate	Amount	Rate J
	Compared of Compared Applies type System by Compared Applies type System by Compared Applies type System by Compared Applies System by	(GST) 8462	1 N	a. 14500	145000.00	45000	100000.00	Rate	Anout	Rate	Amount	18%
	Test State Value (18 Werk) Days by State Value (18 Werk) Days black given the based only				145000.00		100000.0	0	0.00	T	0.00 otal Amount be Ac	efore Tax 64 COST
	Buyer's P.O. No:	Po/2017-1	18/17			- 1		_		_	AL	DD IGST
	P.O.Date: Terms of Payment:	23-11-201	17	170		-	600	-	entified the	To at the partic	tal Amount A	fter Tax
•••	TERMS & CO 01) Goods once sold cannot be taken back 02) All Disputes are subject to Hyderabad	jurisdictio	on				(Seal)		For VA	SANTH.	A HYDRAU	JLIC SV
AREA I												

Hydraulic Press



Smoke Meter

	AA INVOIC	E		Dated
Vs. Logical Solutions Limited	Tax Invoice	No		Dated
1-0-17 (Uld), 1-6-41(New),	LSL/INV/23	-24/5/		Mode/Terms of Payment
haltariyanun Hyderahad - 500 060	Denvery Not	ie i		
elangana India	Supplier's R	ef.		Other Reference(s)
horie 040-24180000, Fax 040-24180001	LSL/INV/23	-24/57		
Mail. info@logicalsolutions in			1	Dated
Buyer ·	Buyer's Ord	er No.	Dent (CAD/CAM	June 18, 2023
0	Lab/PO/202	anical Engg. u	Dept. CADIOA	00000 10, 2020
Ne Sri Vacavi Engineering College	Desnatch D	ocument No.		Dated
Pedatadepalli.	Despotento	ocument in		
Tadepalligudem,	Despatched	through		Destination
West Godavari District - 534101	Electronic	Downloadable.		Tadepalligudem
Andhra Pradesh, India	Terms of D	elivery		
Development (Development	11011	Quantity	Init Price (₹)	Total Price (₹
Description of Goods	UOM	Quantity	Unit / nec (1)	
SOLIDWORKS Education Edition Network License 2023-24 (60 Users)	Nos	1	4,21,000.00	4,21,000.00
			100	S 17 8
IGST @18%				75,780.00
· · ·				
Grand Total				4.96.780.00
Grand Total		1		4,96,780.00
Grand Total Grand Total (in words)		1		4,96,780.00 E. & O. E
Grand Total Amount Chargeable (in words) ₹. Four Lakis Ninety Six Thousand Seven Hundred Eighty Only		1		4,96,780.00 E. & O. E
Grand Total Amount Chargeable (in words) 2. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED'		1	-	4,96,780.00 E. & O. E
Grand Total Amount Chargeable (in words) & Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Comeany's GSTIN: 36AAACL5476G1ZE		1		4,96,780.00 E. & O. E
Grand Total Amount Chargeable (in words) 8. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AACL5476G12E Company's PAN No: AAACL5476G		1	For Logical Solu	4,96,780.00 E. & O. E
Grand Total Grand Total Amount Chargeable (in words) ₹. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G		1	For Logical Solu	4,96,780.00 E. & O. E tions Limited
Grand Total Amount Chargeable (in words) 2. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G		1	For Logical Solu	4,96,780.00 E. & O. E
Grand Total Amount Chargeable (in words) T. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G Declaration		1	For Logical Solut	4,96,780.00 E. & O. E
Grand Total Amount Chargeable (in words) 2. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G Declaration We declare that this invoice shows the actual price of the goods described and		1	For Logical Solu	4,96,780.00 E. & O. E
Grand Total Grand Total Amount Chargeable (in words) 2. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's CSTIN: 35AAACL5476G Company's PAN No: AAACL5476G Declaration We declare that this invoice shows the actual price of the goods described and that all particulars are true and correct.		1	For Logical Solut	4,96,780.00 E. & O. E
Grand Total Grand Total Amount Chargeable (in words) ₹. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's FAN No: AAACL5476G Declaration We declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given bejow our company's Bank Account Details Bank Name HDEC BANK LIMITED		1	For Logical Solut	4,96,780.00 E. & O. E
Grand Total Grand Total Amount Chargeable (in words) 2. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G Declaration We declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given below our company's Bank Account Details Bank Name HDFC BANK LIMITED Current Account No 0415232000290		1	For Logical Solut	tions Limited
Grand Total Grand Total Grand Total Amount Chargeable (in words) T. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's CSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G Declaration We declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given bejow our company's Bank Account Details Bank Name HDFC BANK LIMITED Current Account No: 0418232000290 Company Name LIOGICAL SOLUTIONS LIMITED		1	For Logical Solut	4,96,780.00 E. & O. E
Grand Total Grand Total Grand Total Amount Chargeable (in words) F. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's PAN No: AAACL5476G Declaration Wa declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given below our company's Bank Account Details Bank Name HDFC BANK LIMITED Current Account No 0418232000290 Company ame. :LOGICAL SOLUTIONS LIMITED IFSC Code HDFC0000418		1	For Logical Solut	4,96,780.00 E. & O. E
Grand Total Grand Total Grand Total Grand Total Amount Chargeable (in words) F. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's GSTIN: 36AAACL5476G Declaration Wo declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given below our company's Bank Account Details Bank Name HDFC BANK LIMITED Current Account No 04182320000290 Company Name LDGICAL SOLUTIONS LIMITED IFSC Code HDFC BANK LIMITED IFSC Code HDFC0000418 Branch GADDIANNARAM BRANCH		1	For Logical Solut	4,96,780.00 E. & O. E Hons Limited
Grand Total Grand Total Grand Total Amount Chargeable (in words) T. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G Declaration We declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given bejow our company's Bank Account Details Bank Name HDFC BANK LIMITED Current Account No 0418232000290 Company Name LOGICAL SOLUTIONS LIMITED IFSC Code HDFC0000418 Branch GADDIANNARAM BRANCH HYDERABAD - 500 060		1	For Logical Solut	4,96,780.000 E. & O. E tions Limited
Grand Total Grand Total Grand Total Amount Chargeable (in words) T. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G Declaration We declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given bejow our company's Bank Account Details Bank Name HDFC BANK LIMITED Current Account No 0418232000290 Company Name LOGICAL SOLUTIONS LIMITED INSC Code HDFC0000418 Branch GADDIANNARAM BRANCH HYDERABAD - 500 060 INCOME TAX DECLARATION; We here by declare that software supplied vide his invoice is transfered without any medification and tax has here deducted		1	For Logical Solut	4,96,780.00 E. & O. E
Grand Total Grand Total Grand Total Amount Chargeable (in words) E. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's GSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G2 Declaration We declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given below our company's Bank Account Details Bank Name HDFC BANK LIMITED Current Account No: 04182320000290 Company Name : LOGICAL SOLUTIONS LIMITED IFSC Code HDFC0000418 Branch GADDIANNARAM BRANCH HYDERABAD - 500 060 INCOME TAX DECLARATION; We here by declare that software supplied vide this invoice is transfered without any modification and tax has been deducted Under section 195. Hence, no TDS is to be deducted by you on this invoice		1	For Logical Solut	4,96,780.00 E. & O. E
Grand Total Grand Total Grand Total Amount Chargeable (in words) E. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of 'LOGICAL SOLUTIONS LIMITED' Company's PAN No: AAACL5476G Declaration VW declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given bejow our company's Bank Account Details Bank Name HDFC BANK LIMITED Current Account No 04182320000290 Company Name LOGICAL SOLUTIONS LIMITED It'sSC Code HDFC0000418 Branch GADDIANNARAM BRANCH HYDERABAD - 500 060 INCOME TAX DECLARATION; We here by declare that software supplied vide this invoice is transfered without any modification and tax has been deducted under sochor 195 Hence. on TDS is to be deducted by you on this invoice as per Modification No. 21/2012 [F. No. 142/10/2012-SO(TPL)] S.O. 1323(E), Dated		1	For Logical Solu	4,96,780.00 E. & O. E
Grand Total Grand Total Grand Total Grand Total Amount Chargeable (in words) T. Four Lakhs Ninety Six Thousand Seven Hundred Eighty Only Cheque/DD in favour of "LOGICAL SOLUTIONS LIMITED" Company's GSTIN: 36AAACL5476G12E Company's PAN No: AAACL5476G Declaration Ww declare that this invoice shows the actual price of the goods described and that all particulars are true and correct. Given below our company's Bank Account Details Bank Name HDFC BANK LIMITED Current Account No 04182320000290 Company Name LOGICAL SOLUTIONS LIMITED IFSC Code HDFC BANK LIMITED Current Account No 0418232000290 Company Name LOGICAL SOLUTIONS LIMITED IFSC Code HDFC BANK LIMITED Ne SC Code HDFC BANK DEARCH HYDERABAD - 500 060 NCOME TAX DECLARATION; We here by declare that software supplied vide this invoice is transfered without any modification and tax has been deducted under section 195. Hence, no TDS is to be deducted by you on this invoice as per Notification No 21/2012 [F. No 142/10/2012-SOLPTUS] S O. 1323(E). Dated 13 05 2012 issued by Ministry of Finace. (Central Board of Direct Taxes.)		1	For Logical Solut	4,96,780.00 E. & O. E